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Re Applic of	Cyril Cabral, Jr., et al.
Docket No.	FIS920030263US1
Serial No.	10/605,769
Filing Date	10/24/2003
Attorney	H. Daniel Schnurmann

Attached: Response to Restriction Requirement

PLEASE DELIVER TO: EXAMINER: Ngan V. Ngo ART UNIT: 2814 PHONE NO: FAX NO: 703-872-9306	
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I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING SENT VIA FACSIMILE TRANSMISSION TO: COMMISSIONER OF
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE	
In re application of: Cyril Cabral Jr., et al.	Date: June 7, 2004
Serial Number: 10/605,769	Examiner: Ngan V. Ngo
Filed: 10/24/03	Group Art Unit: 2814
Title: High Performance FET with Laterally Thin Extension	IBM Corporation D/18G, B/300, Zip 482 2070 Route 52 Hopewell Junction, NY 12533-6531

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to the Office Action dated May 28, 2004, the present response is being submitted to elect the following patentably distinct claims of the present invention.

Group I – Claims 1 – 27, drawn to a semiconductor device, classified in Class 257, sub-class 412, and

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Group II – Claims 28 – 40, drawn to a process of making a semiconductor device,
classified in Class 438, subclass 1+.

Applicants hereby elect Group I, claims 1 to 27, drawn to the semiconductor device, for
prosecution in the instant application. Claims 18-40 are withdrawn from consideration.

The election of claims is made without traverse.

Further action with respect to the present application is earnestly solicited.

Respectfully submitted,
CYRIL CABRAL, JR., ET AL.

By:



H. Daniel Schuurmann
Registration No. 35,791
Telephone: (845) 894-2481